



IFW

500.38899VX3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. YAMAMOTO
Serial No.: 10/787,436
Filed: February 27, 2004
Title: METHOD OF MANUFACTURING A DUAL GATE
SEMICONDUCTOR DEVICE WITH A POLY-METAL ELECTRODE
Group: 2812

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 24, 2004

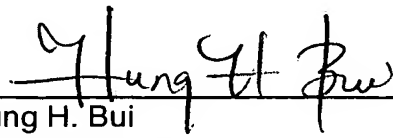
Sir:

Attached hereto is the Official Filing Receipt for the above-identified application, wherein it is noted that the Domestic Priority data is incomplete.

Accordingly, it is respectfully requested that a corrected filing receipt be issued indicating that the Foreign Applications data to be --**DIV of 10/448,351 05/30/2003 PAT 6,7227,132 which is a DIV of 10/272,369 10/17/2002 PAT 6,645,799 which is a DIV of 09/639,306 08/16/2000 PAT 6,503,788--** instead of DIV of 09/639,306 08/16/2000 PAT 6,503,788.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP


Hung H. Bui
Registration No.: 40,415

HHB/rr
Attachments

500.38899VX3

GEM/HHS



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/787,436	02/27/2004	2812	770	500.38899VX3	7	15	3

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CONFIRMATION NO. 4723

FILING RECEIPT



OC000000012686037

Date Mailed: 05/17/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Naoki Yamamoto, Kawaguchi, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/639,306 08/16/2000 PAT 6,503,788

Also -- " " " 10/272,369 10/17/2002 PAT 6,645,799

Foreign Applications " " " 10/448,351 05/30/2003 PAT 6,727,132 --

JAPAN 2000-17875 01/24/2000

If Required, Foreign Filing License Granted: 05/15/2004

Projected Publication Date: 08/26/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing a dual gate semiconductor device with a poly-metal electrode

Preliminary Class

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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